

Notice of References Cited	Application/Control No. 10/709,807	Applicant(s)/Patent Under Reexamination KEMERER ET AL.	
	Examiner Brandon W. Bowers	Art Unit 2825	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,539,652	07-1996	Tegethoff, Mauro V.	703/14
*	B	US-6,397,373	05-2002	Tseng et al.	716/5
	C	US-			
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Holden et al., "Predicting Assembly Costs Before Design with the Design Report Card", Emerging Technologies Conference, November 16, 1998
	V	Gold et al., "A Quantitative Approach to Nonlinear Process Design Rule Scaling", 18th Conference on Advanced Research in VLSI (ARVLSI '99), 21-24 March 1999, Atlanta, GA, USA. IEEE Computer Society 1999
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.